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UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 C.F.R. § 1.53(b))

Attorney Docket No. **P4010NP/CSL**First Inventor or Application Identifier **HIDEKI HIURA**Title **M/S FOR ENABLING MULT. CONCURRENT
SUBPROCESS HANDLING**Express Mail Label No. **EJ110666184US**

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. ☐ * Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original and a duplicate for fee processing)
2. ☒ Specification [Total Pages **15**]
(preferred arrangement set forth below)
- Descriptive title of the invention
 - Cross References to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to Microfiche Appendix
 - Background of the invention
 - Brief Summary of the invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
3. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets **6**]
4. Oath or Declaration [Total Pages **1**]
- a. ☐ Newly executed (original or copy)
 - b. ☐ Copy from a prior application (37 C.F.R. § 1.63(d))
(for continuation/divisional with Box 16 completed)
 - i. ☐ **DELETION OF INVENTOR(S)**
Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §§ 1.63(d)(2) and 1.33(b).

* NOTE FOR ITEMS 1 & 13: IN ORDER TO BE ENTITLED TO PAY SMALL ENTITY FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.27), EXCEPT IF ONE FILED IN A PRIOR APPLICATION IS RELIED UPON (37 C.F.R. § 1.28).

ADDRESS TO:

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5. ☐ Microfiche Computer Program (Appendix)
6. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)
- a. ☐ Computer Readable Copy
 - b. ☐ Paper Copy (identical to computer copy)
 - c. ☐ Statement verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

7. ☐ Assignment Papers (cover sheet & document(s))
8. ☐ 37 C.F.R. § 3.73(b) Statement ☐ Power of Attorney
(when there is an assignee)
9. ☐ English Translation Document (if applicable)
10. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
11. ☐ Preliminary Amendment
12. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
13. ☐ * Small Entity Statement(s) ☐ Statement filed in prior application, Status still proper and desired
(PTO/SB/09-12)
14. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
15. ☒ Other: **Missing Parts Cover Sheet**

16. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No: _____
Prior application information: Examiner _____ Group / Art Unit: _____

For CONTINUATION or DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

17. CORRESPONDENCE ADDRESS

☐ Customer Number or Bar Code Label

(Insert Customer No. or Attach bar code label here)

or ☒ Correspondence address below

Name	John R. Carpenter				
	Patent Attorney				
Address	5520 56th Avenue South				
City	Seattle	State	WA	Zip Code	98118-2515
Country	USA	Telephone	206/725-4782	Fax	206/725-4782

Name (Print/Type)	John R. Carpenter	Registration No. (Attorney/Agent)	38 096
Signature	<i>John R. Carpenter</i>	Date	01/21/00

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U.S.P.S. Express Mail No. EJ110666184US
Attorney Docket No. P4010NP/CSL

COVER SHEET - MISSING PARTS PATENT APPLICATION

Nature of Application: The enclosed application for United States Letters Patent, entitled:

A METHOD FOR ENABLING MULTIPLE CONCURRENT SUBPROCESS HANDLING
ON A SYSTEM USING A GLOBAL PROCESS

is filed here without a declaration and fee. The missing items will be provided in response to a notice of missing parts.

Application Specifics: 15 pages of text (including an abstract and claims; 21 claims, 4 independent); Six figures on six sheets (formal).

Inventorship: Although not explicitly required by 37 CFR Sect. 1.53(b) and MPEP 506.02, the believed inventors per 37 CFR Sect. 1.141(a)(3) are:

Hideki Hiura
141 Del Medio Avenue, #305
Mt. View, CA 94040

Citizenship: Japan

Jenny Huang
110, 303 Building
Ke Xue Yuan Nan Li
Datun Road
Beijing, 100101
P.R. China
Citizenship: P.R. China

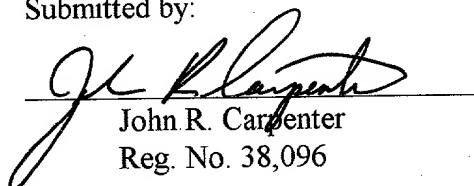
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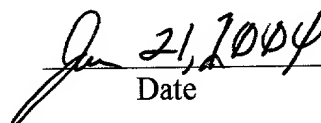
Correspondence: All correspondence should be directed to:

John R. Carpenter, Esq.
5520 - 56th Avenue South
Seattle, WA 98118-2515

Patent Reg. No. 38,096
206.725.4782 (voice and fax)
johnncarpenter@prodigy.net

Submitted by:


John R. Carpenter
Reg. No. 38,096


Date

0 **FILED: January 21, 2000**

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Inventors: Hideki Hiura & Jenny Huang

5
**A METHOD FOR ENABLING MULTIPLE CONCURRENT SUBPROCESS HANDLING
ON A SYSTEM USING A GLOBAL PROCESS**

10 **BACKGROUND OF THE INVENTION**

Field of the Invention

The present invention relates generally to computer systems, and more specifically to multiple concurrent subprocess handling on systems employing global processes using a communications protocol that does not require data serialization or deserialization.

Related Art

To understand the present invention, one must first understand the relationship between a global processes and its subprocesses. Consider the case where a global process is accessed by a variety of client or user processes. When the global process needs to analyze user-specific data for a variety of such users, it does so by way of a subprocess. In a concurrent subprocess system, many subprocesses of a single global process are simultaneously active.

Locales in a Unix environment provide a practical example of global processes and subprocesses. A locale defines a set of language conventions by relative to language territory and code set combinations. These conventions include information on character classification, case conversion, date and time representation, monetary symbols, and numeric representations. One can think of locales, as including all the information necessary to communicate relay information in a given language. Thus, one sometimes speaks of locales in terms of region or language, such a Dutch locale or a Japanese locale.

ANSI-C is a version of the C programming language that adheres to standards

0 set forth by the American National Standards Institute. The Portable Operating System Interface for Unix (POSIX) is a standard that defines operating system devices. A common conglomeration of these standards is known as the ANSI-C/POSIX standard. This standard utilizes a global locale environment. What this means is that each process or task is bounded to a single global locale.

5 A system having a global locale environment is illustrated in Figure 1, which illustrates a server system 100. The system 100 includes a central processing unit 102; a primary memory 104, such as a random access memory, for program execution; and a secondary memory 106, such as a disk, for storing programs and data that are not immediately needed for execution. Contained within memories 104, 106 are an
10 operating system 108 that utilizes a global locale. Also pictured is the kernel 110, which is the core of the operating system 108.

The kernel 110 has a single process for each differing locale, 150, 152, 154, and a single process for each differing user or client, even if that client utilizes the same locale 156, 158, 160. These single processes 150, 152, 154, 156, 158, 160 are created regardless of the amount of processing functionality they share. That is, the processes' instructions could be 90% locale-independent and largely functionally similar to each of the other processes. Nevertheless, these separate processes must be created under a global locale standard, such as ANSI-C/POSIX. Unfortunately, each such process consumes precious computing resources: memory, which is directly consumed, and processing power, which is indirectly consumed when many processes occupy substantial amounts of memory.

In contexts such as this and others, past techniques have attempted to serve concurrent subprocesses using a variety of communication techniques, such as remote procedure calls and remote method invocations. These methods are limited, however,
25 because they require the serialization and de-serialization of all data, including pointers used between the processes and subprocesses. This is costly in terms of processing time. The present invention provides a better way.

INVENTION SUMMARY

Embodiments of the present invention provide methods and systems for

0 enabling multi-subprocess handling on systems employing a global or master process.
In a preferred embodiment, a virtual memory separator interfaces with a master process
and an operating system kernel. This separator utilizes the master process in
conjunction with user-specific processes to enable concurrent subprocess handling.
The virtual memory separator maps each user-specific process so that it virtually
5 overlays the master processes' address space. In a preferred embodiment, this
mapped user-specific process is partially empty because it contains only the data and
coding to perform user-specific operations. When user-specific instructions are
encountered in the master process, processing is transferred to the user-specific
process. Data is efficiently transferred because each user-specific process has an
10 interface that is identical to its master process: because the subprocesses virtually
overlay the addresses of the master process, there is no need to serialize and de-
serialize data, including pointer information. As such, the present invention provides
for high performance communications in concurrent subprocessing environments.

BRIEF DRAWING DESCRIPTIONS

The accompanying drawings illustrate embodiments of the invention that will
enable those skilled in the art to make and utilize the invention. In fact, these drawings,
when considered with the written disclosure, will enable those skilled in the art to
modify these embodiments in a manner that does not depart from the spirit and scope
of the invention.

Figure 1 depicts a prior art computer system.

Figure 2 depicts a computer system in accordance with a preferred embodiment
of the present invention.

Figure 3 is a conceptual illustration of interactions between a master process
and its concurrent subprocesses in a preferred embodiment of the present invention.

Figure 4 is a flowchart illustrating steps used in an embodiment of the present
invention.

Figure 5 is a conceptual illustration of interactions between a master process
and its constituent concurrent subprocesses in an alternate preferred embodiment of
the present invention.

0 Figure 6 depicts a computer system that provides for multiple subprocess handling on a system using a global process in accordance with an alternate preferred embodiment of the present invention.

DETAILED DESCRIPTION

5 Embodiments of the invention will now be described with reference to the accompanying drawings. It is understood that the invention is not limited to these embodiments, as the teachings contained herein will enable others to modify these embodiments without departing from the spirit and scope of the invention. Accordingly, the invention encompasses all such modifications that are described by the appended claims and their equivalents.

Overview of The Detailed Description

10 The preferred embodiments of the present invention enable a global process system to operate as concurrent, multiple subprocesses. In part, the detailed description that follows illustrates preferred embodiments that employ the present invention in a global locale environment. Those skilled in the art will appreciate, however, that the invention is not limited to locale-based embodiments. On the contrary, this disclosure will enable those skilled in the art to create versions of the invention that relate to a variety of master process/subprocess technologies.

20 The structure of the disclosure is as follows: A computer system embodying preferred embodiments of the present invention is discussed. See Figure 2. An illustration of a concurrent locale system as practiced in a preferred embodiment is next described. See Figure 3. The disclosure continues with a detailed explanations of processing steps of in a preferred embodiment. See Figures 4A, 4B. Next an alternate preferred embodiment, one using adaptive virtual memory separation, is described. See Figures 5, 6.

Figure 2: Computer System

25 A computer system that enables concurrent locales in a global locale computer system is illustrated in Figure 2. This embodiment illustrates a system 200 that includes a client computer 202 connected to a server computer 204 via a network 206.

The client system 202 includes a standard array of components: a central

0 processing unit 208; a user interface 210, which typically consists of a keyboard, a
mouse, and a monitor; a modem 212, a primary memory 214, such as a random access
memory, for program execution; and a secondary memory 216, such as a disk, for
storing programs and data that are not immediately needed for execution. Contained
within memories 214, 216 are an operating system 218 that contains instructions and
5 data for managing the computer's resources and a client program.

Like the client system 202, the server system 204 contains a CPU 222 and an
operating system 224 stored in its primary and secondary memories 226, 228. In this
embodiment, the operating system contains a kernel 230 that uses a global locale
system, such as that defined by the ANSI-C/POSIX standard. Those skilled in the art
10 will appreciate that the invention is not limited to any particular operating system.
Indeed, while a valuable aspect of the preferred embodiment is that it can extend global
locale systems to concurrent locale systems, the preferred embodiment can run on any
operating system that has memory mapping capabilities. Interacting with the kernel
230 is a virtual memory separator 232, which can contain an adaptive virtual memory
separation component 234. In one preferred embodiment, the virtual memory
separator 232 interfaces with an unmodified ANSI-C/POSIX compliant kernel. Since
the kernel 230 has not been modified, the virtual memory separator 232 is highly
portable. This adaptive virtual memory separation component is explained in detail
with reference to Figures 5, 6.

20 Figure 3: Conceptual Illustration of Concurrent Locales

Two conceptual illustrations of concurrent locales as practiced in a preferred
embodiment are illustrated in Figure 3. A process is said to be virtual memory
separator-enabled when it accesses the virtual memory separator to complete its
constituent subprocesses. The VMS-enabled process allocates memory for a master
25 process 302, which interacts with the kernel 230 and performs the non-locale
dependent operations. The VMS-enabled process also creates subprocesses, each of
which the virtual memory separator maps in a largely empty virtual process space. As
illustrated, the subprocesses are locale-specific processes for a variety of locales. The
separator maps a locale-specific process for each desired locale: e.g., one for Russian

0 a keystroke event, one appreciates why this is the case.

Figure 4: Virtual Memory Separator Flowchart

1 The processing steps used in a preferred embodiment of the present invention to
enable concurrent locales in a global locale system is illustrated in Figure 4. The
process begins when a user, such as client program 220, outputs data, such as a
5 string, binary data, or a keyboard or mouse event, to the master process on the server
side (step 402). If this data is from a new user, the separator maps a new locale-
dependent process to the same address as the VMS-enabled master process (steps
404, 406, 408). Else, processing continues directly in the master process, which
performs locale-independent operations (step 410). When locale-dependent
10 operations are encountered in the master process, those operations are executed in
the subprocesses of the respective locale-dependent processes (steps 412, 414). For
example, if a thread belonging to a Spanish client encounters locale-specific
subprocesses of the master process, program control is transferred to the Spanish
locale-dependent process. Data is shared via the previously described communication
channel and there is no need to serialize the data due to the identical interfaces shared
by the master and locale-dependent processes and the identically mapped virtual
addressing. Once the locale-dependent subprocess has the necessary data, the
locale-dependent operations are performed in the respective locale-dependent
subprocess. When the dependent operations are completed, control is returned to the
master process (step 414, 416, 418). In this manner, processing continues until
20 complete. Upon completion, the virtual memory separator returns data to the user
(steps 418, 420).

Figure 5: Conceptual Illustration of Concurrent Subprocesses using Adaptive Methodology

25 A conceptual illustration of concurrent locales in an alternate embodiment of the
present invention is shown in Figure 5. The illustration is similar to the embodiment
previously described in that it utilizes a master process 502 that interacts with user-
dependent processes 504, 506 that are mapped over the master process by the virtual
memory separator. As mentioned above, these user-dependent processes 504, 506

0 could be locale-dependent or part of another master process/subprocess scheme, such
as those prevalent in service daemons. The embodiment differs from that described
above in that it creates the user-dependent process "on-the-fly"--that is, when the user-
specific encoding is encountered. For example, when user-specific encoding 508 is
encountered in the master process 502, the virtual memory separator creates a
5 communication channel 510, 512 to transfer data between the master process 502 and
locale-dependent process 508. The user-specific operations are then completed in the
subprocess 514. Once that processing is done, control is returned to the master
process 502. Processing here continues until other user-dependent functionality 516 is
encountered. When that happens, an additional subprocess is "spun-off": a
10 communication channel is again opened and the subprocess is created. The
channel/subprocess 518 can be created in the same mapped user-dependent process
504, or it 520 can be created in a separate user-dependent process 506. Either
technique allows for the servicing of subprocesses within a master or global process
environment. And because the subprocesses are mapped to virtual addresses that are
identical to the master process and use the same interface as the master process, the
embodiment avoids the costly serialization/de-serialization processing overhead that
plagues conventional systems.

Figure 6: Adaptive Virtual Memory Separator Flowchart

20 The processing steps used in an alternative preferred embodiment of the
present invention to enable concurrent subprocesses in a global process system is
illustrated in Figure 6, which further explains the adaptive concept explained with
reference to Figure 5. Processing begins when a user outputs data to a VMS-enabled
master process, where user-independent operations are performed (steps 602, 604,
606). When user-specific encoding is encountered, the master process creates a user-
25 specific process that the virtual memory separator maps to a user-specific process
(step 608, 610). As in the embodiment explained above, the user-specific process
shares the same interface and virtual addressing with its master process. In this
embodiment, the user-specific process is largely an empty space--excepting the area
that contains the user-specific subprocess, which is defined by user-specific

0 instructions. Next, the user-specific instructions are executed in the user-specific subprocess (step 612). When this is done, processing is returned to the master process. Processing continues in this fashion until complete (steps 614, 606, 608, 610, 612), at which point data is returned to client (steps 616, 618).

5 The Spirit and Scope of the Invention is Broad: It is not limited to the described embodiments

10 The above description will enable those skilled in the art to make numerous modifications to the described embodiments without departing from the spirit and scope of the claimed invention. Indeed, the chosen embodiments were selected so others could best utilize the invention by making such modifications to tailor the invention to their particular needs. By using the inventive concepts disclosed herein, those skilled in the art will be able to create further such embodiments that, although not explicitly disclosed, are implicitly disclosed by the concepts of the invention. The detailed description therefore should not be read as limiting the invention to the embodiments explained herein.

15 For example, this disclosure illustrated a computer system having a distinct server and client computer. Those skilled in the art will realize that the invention can be practiced on a single computer that uses master/subprocess handling.

20 Descriptive terms used in the disclosure also do not limit the invention. For example, the terms "master process," "subprocess," "global locale system," "locale-specific process," and "user-specific subprocesses" were all used to help describe preferred embodiments and to provide an enabling disclosure. While helpful for an understanding of the invention, these terms should not be read to limit the invention, as doing so would elevate form over substance. For these descriptive terms do not singularly define any embodiment of the invention, let alone the invention itself. Rather, it is the properties that these terms represent, in conjunction with the entire disclosure, that are important. So long as the inventive properties are being used, it makes little difference what terminology is invoked. For example, although one preferred embodiment explained the invention's use a global locale system, the inventive concepts extend far beyond that area. As mentioned previously, the

0 embodiments could be extended to a variety of master/subprocess concepts, such as
service daemons.

 Likewise, although portions of the disclosure referred to the ANSI-C/POSIX
standard, those skilled in the art will recognize that the inventive concepts are not
constrained to an operating system that adheres to this standard. On the contrary,
5 embodiments can be extended to any operating system that employs memory mapping.
Thus, the invention can be used in a variety of operating environments, such as UNIX,
MacOS, LINUX, WINDOWS NT, WINDOWS 95/98/2000, the X-Windowing system or
any JAVA runtime environment, which refers to the operating environment typified by a
JAVA virtual machine and associated JAVA class libraries. JAVA is a registered
10 trademark of SUN MICROSYSTEMS, INC.

 The present invention is also not limited to any particular CPU 102 or processing
technology. Similarly, although the inventive concepts were described in part as being
contained within random access memory and a hard disk, the present invention is not
limited to these devices. Those skilled in the art will recognize that these concepts can
also be stored and invoked from any media that can store data or have data read from
it. Examples of such media include floppy disks, magnetic tapes, phase discs, carrier
waves sent across a network, and various forms ROM, such as DVDs and CDs. The
present invention anticipates the use of all computer readable media.

 Infringement is also not escaped by altering the order of the steps as they
20 appear in the appended claims--for unless explicitly declared otherwise in the claim,
the order set forth is of no significance. The invention therefore is not circumvented by
alterations in terminology or other modifications that read on the appended claims and
their equivalents.

Year	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100
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interfacing with a master process when a user-specific operation is encountered;
mapping a user-specific process so that it overlays virtual addresses of the
r process; and
processing the user-specific operation in the user-specific process.

3. The method of claim 1, further comprising the step of:
providing an interface for the user-specific process that mirrors an interface for master process.

5. The method of claim 1 wherein the user-specific process is mapped after the specific operation is encountered.

7. The method of claim 1 further comprising the step of:
returning processing to the master process after processing the user-specific
operation in the user-specific process.

8. A computer-readable medium containing computer instructions that facilitate concurrent handling of subprocesses in a system that utilizes a global process, the medium comprising:

instructions that, when executed, provide for the mapping of a plurality of concurrent user-specific processes, wherein each user-specific process is mapped to virtual addresses that are equivalent to virtual addresses of the global process.

9. The computer-readable medium of claim 8, further comprising:

instructions that, when executed, provide each of the plurality of concurrent user-specific process with an interface that is identical to an interface of the global process.

10. The computer-readable medium of claim 9, further comprising:

instructions that, when executed, provide for the mapping of a subprocesses within each of the plurality of user-specific processes, the subprocesses being mapped to virtual addresses that are equivalent to virtual addresses for user-specific operations of the global process.

11. The computer-readable medium of claim 10, further comprising:

instructions that, when executed, provide for the return of processing to the global process after execution of the subprocesses is complete.

12. A computer system for enabling concurrent multiple subprocess handling in a global process environment, the system comprising:

a global process; and

a virtual memory separator that maps a user-dependent process to virtual addresses that mirror virtual addresses of the global process, the user-dependent process having an interface that mirrors an interface of the global process.

13. The computer system of claim 12 wherein the global process is a global

locale process and wherein the user-dependent process is a locale-dependent process.

14. The computer system of claim 12 wherein the global process is a global daemon process and wherein the user-dependent process is a user-dependent daemon process.

15. An apparatus for conducting multi-user concurrent handling of subprocesses, the apparatus comprising:

means for interfacing with a master process when a user-specific operation is encountered;

means for mapping a user-specific process so that it overlays virtual addresses of the master process; and

means for processing the user-specific operation in the user-specific process.

16. The apparatus of claim 15, further comprising:

means for transferring data between the master process and the user-specific process using a communications channel that does not require the serialization of data.

17. The apparatus of claim 15, further comprising:

means providing an interface for the user-specific process that mirrors an interface for the master process.

18. The apparatus of claim 15 wherein the master process is a global locale process and the user-specific process is a locale-dependent process.

19. The apparatus of claim 15 wherein the user-specific process is mapped after the user-specific operation is encountered.

20. The apparatus of claim 15 wherein the user-specific process is mapped

before the user-specific operation is encountered.

21. The apparatus of claim 15, further comprising:

means for returning processing to the master process after the user-specific operation is executed in the user-specific process.

ABSTRACT

A method and system for enabling multi-subprocess handling on computer systems that employ a global process. A virtual memory separator is provided as part of an operating system to interface with a master process and a kernel of the operating system. The separator maps user-specific processes to virtual address spaces that mirror that of the global process. These user-specific processes are empty spaces, excepting their interface—which is identical to that of the global process—and instructions necessary to carry out user-specific processing. When user-dependent operations are encountered in the global process, execution is transferred to a respective user-specific process. Since each user-specific process shares addresses and interfaces with the global process, data can be exchanged between them without serialization, which reduces processing overhead.

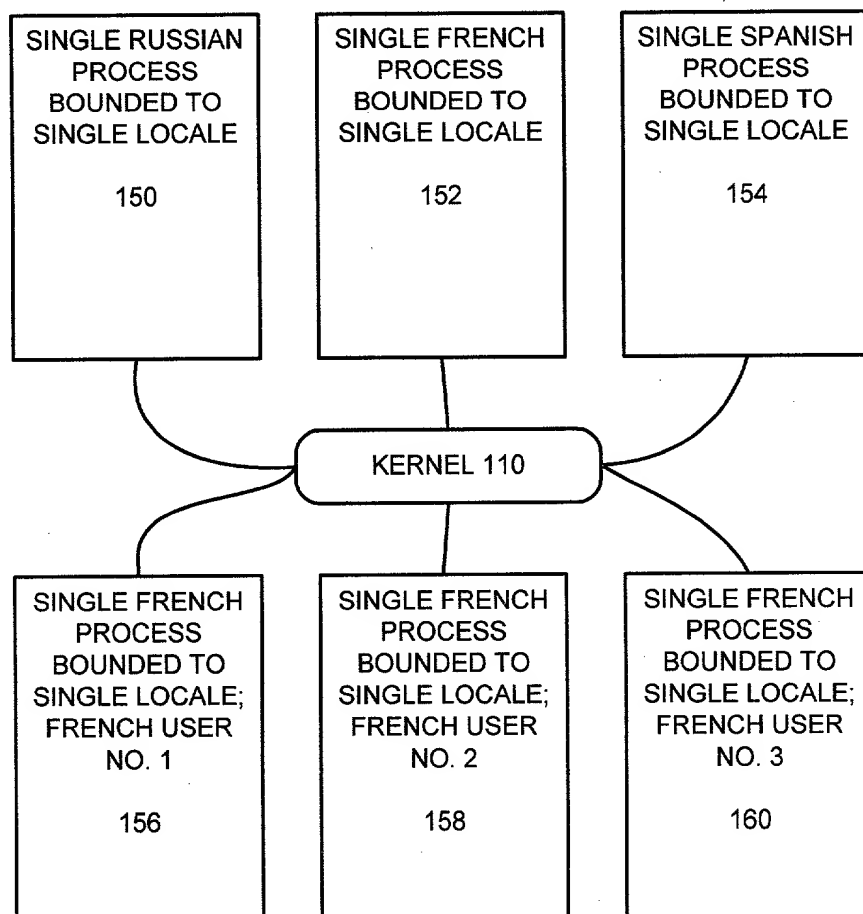
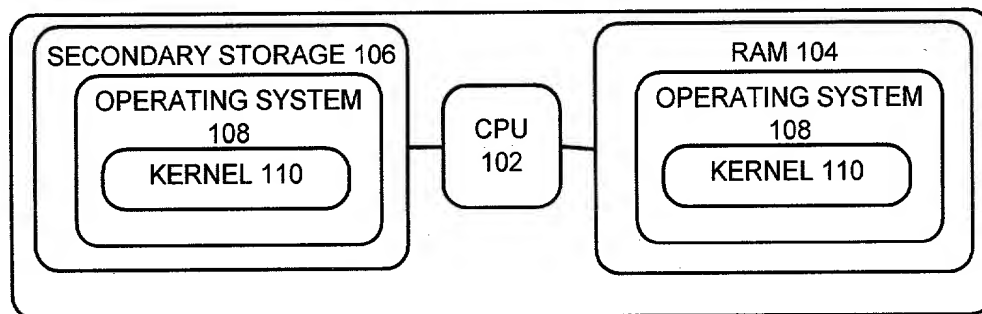
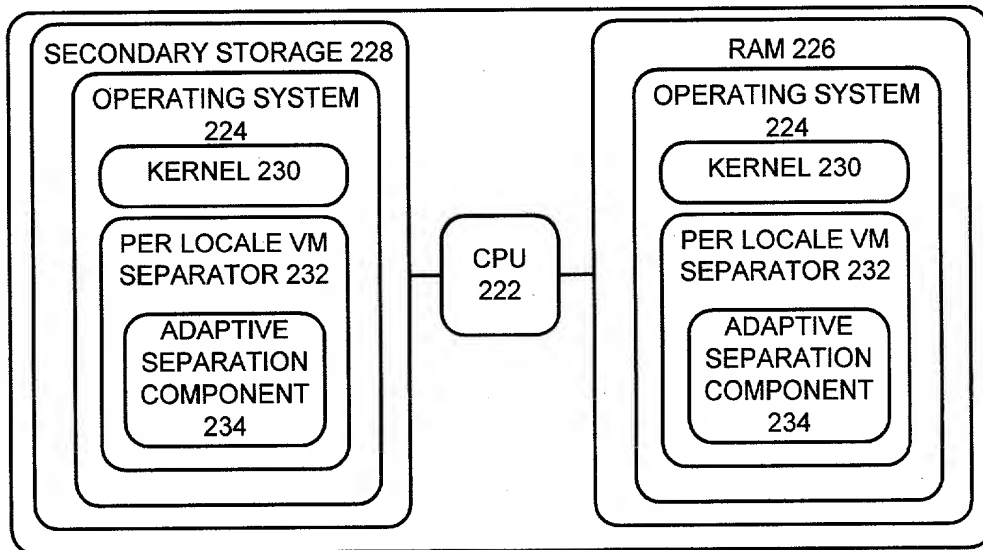


FIGURE 1 (PRIOR ART)

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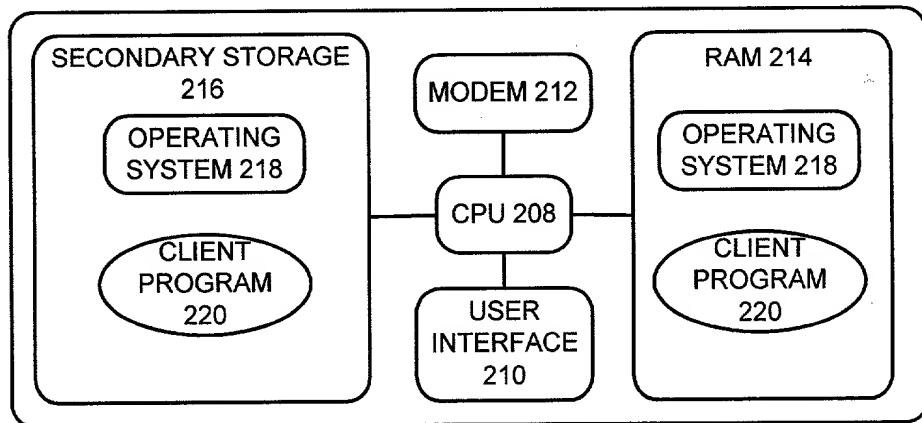
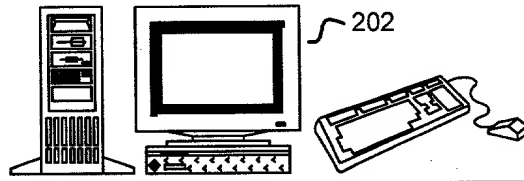
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204



202

FIGURE 2

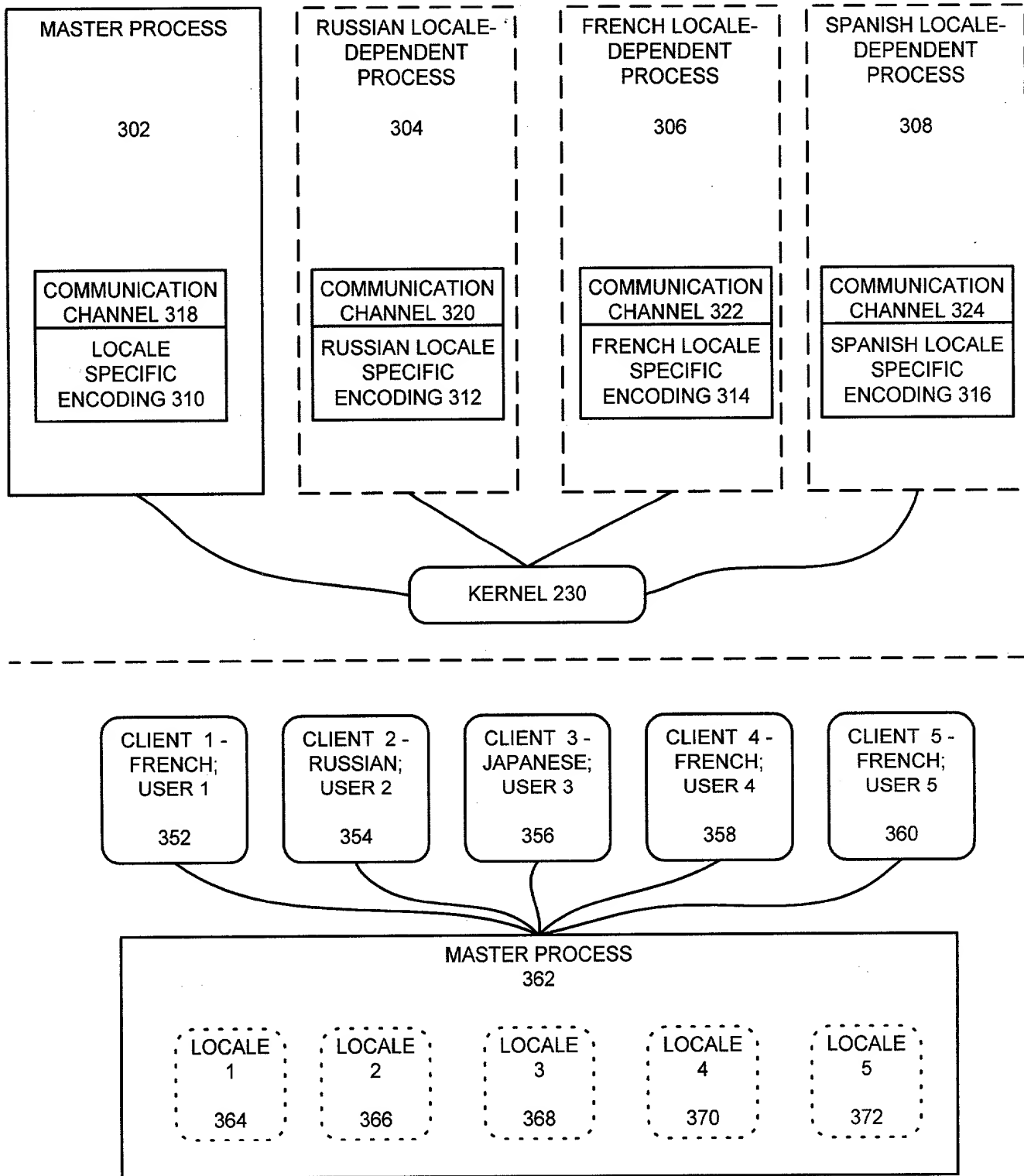


FIGURE 3

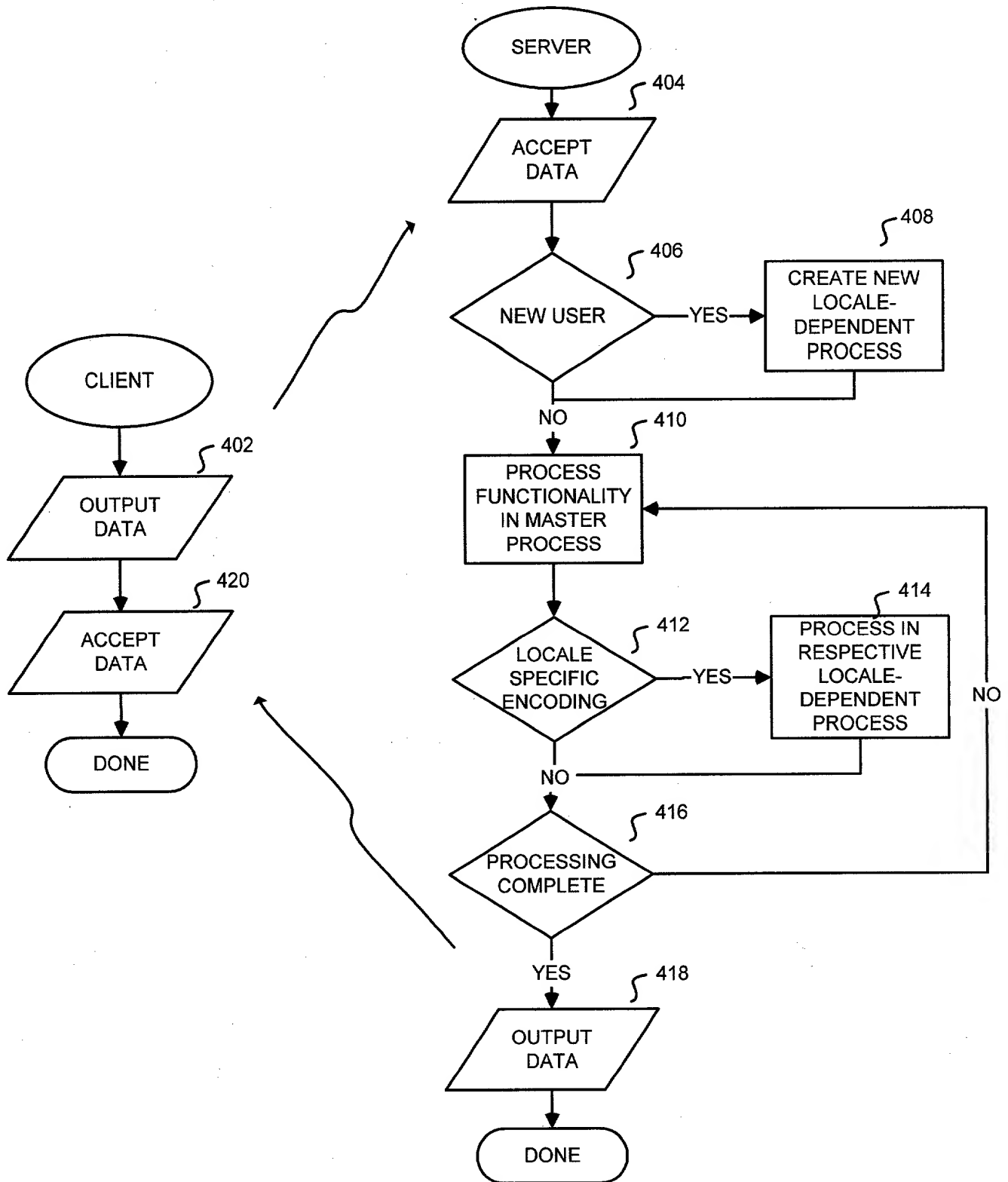


FIGURE 4

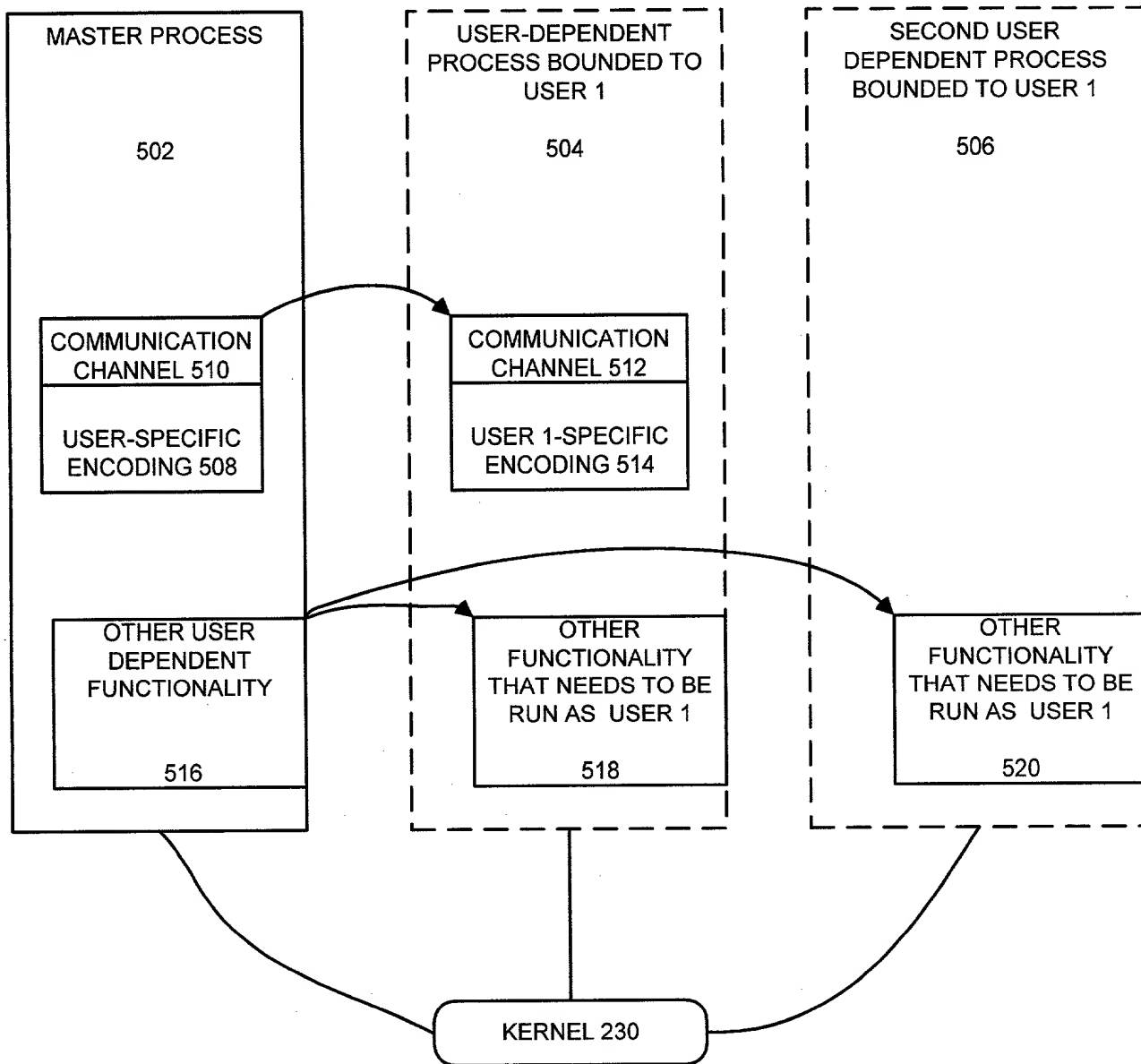


FIGURE 5

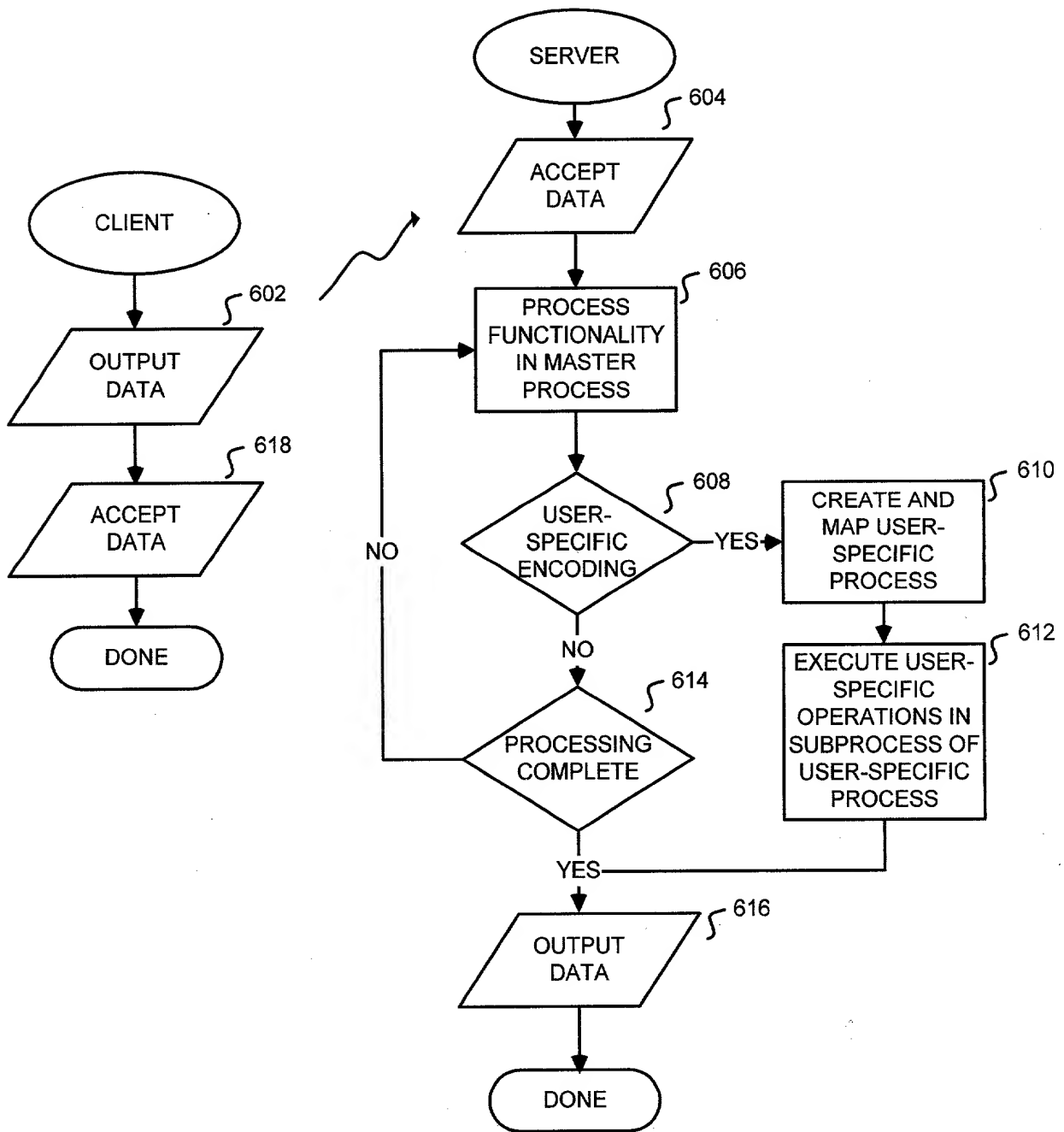


FIGURE 6